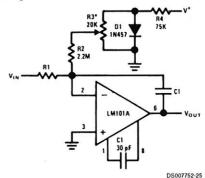
## Typical Applications (Note 9) (Continued)

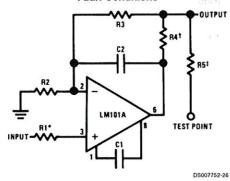
#### Integrator with Bias Current Compensation



\*Adjust for zero integrator drift. Current drift typically 0.1 nA/'C over -55°C to +125°C temperature range.

## **Application Hints** (Note 9)

Protecting Against Gross Fault Conditions

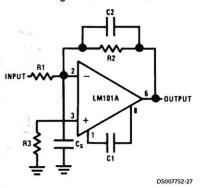


Protects input

†Protects output

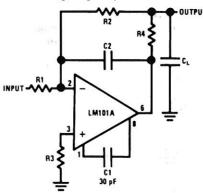
‡Protects output - not needed when R4 is used.

# Compensating for Stray Input Capacitances or Large Feedback Resistor



 $C2 = \frac{R1 C_S}{R2}$ 

### Isolating Large Capacitive Loads



DS007752-2

Although the LM101A is designed for trouble free operation, experience has indicated that it is wise to observe certain precautions given below to protect the devices from abnormal operating conditions. It might be pointed out that the ad-

vice given here is applicable to practically any IC op amp, although the exact reason why may differ with different devices.